

DESCRIPTION

METHOD FOR MANUFACTURING CERAMIC SUBSTRATE

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Technical Field

The present invention relates to a method for manufacturing a ceramic substrate.

Background Art

10 A low-temperature-sintered ceramic (LTC) substrate among multi-layer ceramic circuit substrates is recently used for down-sized components in electronic appliances such as personal computers or cellular phones. This type of the multi-layer ceramic circuit substrate is formed by a method in which ceramic green sheets (GS's) which is not sintered are stacked, i.e., a
15 GS stacking method. As a method of forming conductive patterns, a screen printing method is generally applied for forming internal interconnections. A screen printing method, thin film photolithography or thick film photolithography is generally known for forming surface electrical connections on ceramic substrates.

20 Appliances such as cellular phones require down-sized, light electronic components. An LCR hybrid circuit substrate which forms a filter including inductors, capacitors, and resistors incorporated in a multi-layer ceramic substrate thereof is developed as a printed circuit board. A fine line pattern technology is required because the elements such as LCR and internal
25 conductive patterns are formed by a screen printing method.

A current screen printing method, however, can provide a line pitch (wire diameter + clearance) of 100 μ m at the narrowest at a mass-production

stage.

A conventional method for manufacturing multi-layer ceramic circuit substrates formed with green sheets will be described with referring to Fig.9. At step 9a, through-holes 92 are punched in an un-sintered ceramic green sheet 91 with a puncher, piercing die or YAG laser apparatus or the like. At step 9b, the through-holes 92 of the green sheet 91 are filled with conductive paste, and via-hole conductors 93 are then formed by a screen printing method or the like with metal screen plates. At step 9c, conductive patterns 94 are formed on the green sheet 91 with conductive paste by a screen printing method. At step 9d, plural green sheets 91 are stacked and heat-pressed, thus being integrated. At step 9e, a de-binder process for removing a binder and a sintering process are applied to the stacked assembly. Then, the green sheet 91 becomes a sintered ceramic substrate 95, and the via conductors 93 and conductive patterns 94 become sintered via conductors 96 and sintered conductive patterns 97, respectively. The conductive pattern has a thickness ranging approximately 7 to 8 μ m.

The conventional screen printing method can provide a pitch of 100 μ m at the narrowest in internal conductive patterns

As a method for forming high-density conductive patterns on ceramic substrates, a film-intaglio-transfer-printing method is well known. The printing method provides both very fine line patterns (approximately 40 μ m pitch between wires) and a high aspect ratio (approximately 10 μ m thickness after the sintering). The film-intaglio-transfer-printing method can be applied to manufacturing ceramic circuit substrates in two manners.

In one manner, a multi-layer ceramic circuit substrate is manufactured by stacking the GS's having conductive patterns formed by the film-intaglio-transfer-printing method. In the other manner, the multi-

layer ceramic circuit substrate is manufactured by stacking sintered ceramic substrates having conductive patterns formed by the film-intaglio-transfer-printing method and un-sintered green sheets alternatively.

When, however, the conductive patterns are transfer-printed directly onto the GS's by the film-intaglio-transfer-printing method, a surfaces of the GS's are necessarily coated with an adhesive layer composed mainly of thermoplastic resin. However, the GS's can not be coated with the adhesive layer because an organic solvent dissolves the adhesive layer. In addition to this, the un-sintered green sheets and sintered ceramic substrates, upon being stacked alternatively, may hardly be adhered with each other, and thus, may be de-stacked between the sheets during being heat-pressed, having the binder removed, or being sintered.

Disclosure of the Invention

15 A method of manufacturing a ceramic circuit substrate having a fine conductive pattern as an internal conductive pattern formed by a film-intaglio-transfer-printing method is provided.

The manufacturing method includes the following steps:

20 (a) Filling an intaglio with a conductive paste to form the conductive pattern by the film-intaglio-transfer-printing method;

(b) Forming an adhesive layer on a heat-resistant substrate;

(c) Heat-pressing the intaglio on the adhesive layer

(d) Removing the intaglio; and

(e) Forming an assembly through heat-pressing an un-sintered green

25 sheets covering the conductive pattern.

The assembly has a binder removed and is sintered, and thus, provides a ceramic substrate.

Brief Description of the Drawings

Fig. 1 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 1 of the present invention.

Fig. 2 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 2 of the invention.

Fig. 3 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 3 of the invention.

Fig. 4 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 4 of the invention.

Fig. 5 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 5 of the invention.

Fig. 6 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 6 of the invention.

Fig. 7 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 7 of the invention.

Fig. 8 illustrates sectional views of a ceramic substrate in a manufacturing process according to Embodiment 8 of the invention.

Fig. 9 illustrates sectional views of a ceramic substrate in a conventional manufacturing process.

Description of the Preferred Embodiments

(Embodiment 1)

Fig. 1 illustrates a method for manufacturing a ceramic circuit substrate in accordance with exemplary embodiment 1 of the present invention. A conductive pattern is formed on an un-sintered ceramic green sheet (hereafter referred to as GS) by a film-intaglio-transfer-printing

method.

At step 1a, a polyimide intaglio 11 having grooves formed with excimer laser has the grooves filled with conductive paste 12 with a ceramic blade or the like. As the volume of the conductive paste shrinks when being dried, 5 the intaglio 11 is filled with the conductive paste and dried repeatedly until the dent at the paste has a depth of 5 μ m or less. At step 1a, an adhesive layer 14 composed mainly of thermoplastic resin is applied on a heat-resistant substrate 13 capable of resisting against a temperature of 150°C or higher having a thickness of approximately 1 to 10 μ m. At step 1b, the 10 intaglio 11 filled with the conductive paste 12 is heat-pressed onto the heat-resistant substrate 13. At step 1c, the intaglio 11 is separated from the heat-resistant substrate 13 after the conductive paste 12 is transfer-printed. At step 1d, an un-sintered ceramic GS 15 is stacked onto the heat-resistant substrate 13 at a side having the conductive paste 12 transfer-printed by 15 being heat-pressed onto the side. At step 1e, the heat-resistant substrate 13 is separated from the GS 15. At this moment, the conductive pattern 12 is embedded onto the GS 15, and thus, is transfer-printed again and secured. At step 1f, the GS 15 having the transfer-printed conductive pattern 12 has a binder removed and is sintered. Then, the GS 15 becomes a sintered 20 ceramic substrate 17, and the conductive paste 12 becomes a sintered conductive pattern 16.

As described above, the conductive pattern is easily formed on the un-sintered ceramic green sheet by the film-intaglio-transfer-printing method.

25 (Embodiment 2)

Fig.2 shows processes of manufacturing a ceramic multi-layer circuit substrate in accordance with exemplary embodiment 2 of the present

invention. In Fig.2, an un-sintered ceramic green sheet (hereafter referred to as GS) having a conductive patterns formed thereon by a film-intaglio-transfer-printing method described in embodiment 1.

At step 2a, a GS 21 having a conductive pattern formed thereon by the 5 film-intaglio-transfer-printing method is coated with an adhesive 23 on the side having a conductive paste 22. At step 2b, a necessary number of the GS's 21 are stacked by heat-pressing for being integrated. At step 2c, the stacked GS's have a binder removed and is sintered. The GS's 21 upon shrinking after the sintering, becomes sintered ceramic substrates 25, and 10 the conductive paste 22 similarly becomes a sintered conductive pattern 24. The adhesive 23 is burnt off during the sintering. The sintered ceramic substrates 25 are coupled tightly with each other.

As described above, according to embodiment 2, a ceramic multi-layer circuit substrate is easily manufactured with un-sintered ceramic green 15 sheets having conductive patterns formed by the film-intaglio-transfer-printing method.

(Embodiment 3)

Fig.3 shows a method of manufacturing a ceramic circuit substrate in 20 accordance with exemplary embodiment 3 of the invention. A conductive pattern is formed on an un-sintered ceramic green sheet (hereafter referred to as GS) having a via conductor formed by the film-intaglio-transfer-printing method.

At step 3a, a through-hole 32 is formed in a GS 31 with a puncher or 25 the like. At step 3b, a conductive pattern 35 formed with an intaglio 34 is transfer-printed onto a heat-resistant substrate 36 having surfaces coated with an adhesive layer 37. At step 3b, a via conductor 33 is formed in the

through-hole 32 of the GS 31. At step 3c, the GS 31 having the via conductors 33 is heat-pressed onto the heat-resistant substrate 36 having the conductive patterns 35 formed by the film-intaglio-transfer-printing method. At step 3d, the heat-resistant substrate 36 is separated from the 5 GS 31. Then, the conductive pattern 35 formed by the film-intaglio-transfer-printing method is attached on GS 31 with being embedded in the GS, and additionally, the adhesive layer 37 on the heat-resistant substrate 36 is attached onto the GS. At step 3e, the GS 31 has a binder removed and is sintered. The GS 31 shrinks after the sintering and becomes a sintered 10 ceramic substrate 39, and the conductive pattern 35 similarly becomes a sintered conductive pattern 38.

(Embodiment 4)

Fig.4 shows a method of manufacturing a ceramic substrate in 15 accordance with exemplary embodiment 4 of the present invention. An unsintered ceramic green sheet (hereafter referred to as GS) is stacked a sintered ceramic substrate having conductive patterns formed thereon by a film-intaglio-transfer-printing method.

At step 4a, a polyimide intaglio 41 having a groove formed with 20 excimer laser has the groove filled with conductive paste 42 with a ceramic blade or the like. As the volume of the conductive paste shrinks when being dried, the intaglio 41 is filled with the conductive paste and dried repeatedly until a dent at the paste has a depth of 5 μ m or less. At step 4a, a sintered ceramic substrate 43 is coated with an adhesive layer 44 composed mainly of 25 thermoplastic resin at a thickness of approximately 1 to 10 μ m. At step 4b, the intaglio 41 filled with the conductive paste 42 is heat-pressed onto the sintered ceramic substrate 43. At step 4c, the intaglio 41 is separated from

the sintered ceramic substrate 43 after the conductive paste 42 is transfer-printed. At step 4d, the GS 45, upon being heat-pressed onto the substrate 43, is stacked on the sintered ceramic substrate 43 at the side where the conductive paste 42 is transfer-printed. In this process, the un-sintered 5 ceramic GS 45 and sintered ceramic substrate 43, since bonded more sufficiently with the adhesive layer 44, do not remove form each other. At step 4e, the GS 45 has a binder removed and is sintered. The adhesive layer 44 coated on the sintered ceramic circuit substrate 43 is burnt off during the sintering. After the sintering, the GS 45 shrinks, and thus, 10 becomes a sintered ceramic substrate 47, and the conductive paste 42 becomes a sintered conductive circuit pattern 46. The ceramic circuit substrate manufactured by this method exhibits little shrinkage in a direction in its surface, but exhibits large shrinkage in a direction of its thickness.

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(Embodiment 5)

Fig.5 shows a method of manufacturing a ceramic substrate in accordance with exemplary embodiment 5 of the present invention. Un-sintered ceramic green sheets (hereafter referred to as GS) and sintered 20 ceramic substrates having conductive patterns formed by a film-intaglio-transfer-printing method are alternatively stacked.

At step 5a, a conductive paste 52 filled in a grooves of an intaglio 51 is transfer-printed onto a sintered ceramic substrate 53 coated with an adhesive layer 54. At step 5b, an un-sintered ceramic GS 55 is placed 25 between the two sintered ceramic substrates 53 each having the conductive paste 52 formed by the-film-intaglio-transfer-printing method. At step 5c, the stacked structures are heat-pressed for being integrated. Consequently,

a thickness (protrusion) of the conductive paste 52 formed on the sintered ceramic substrate 53 is embedded in GS 55 like being absorbed. At step 5d, the GS has a binder removed and is sintered. The Adhesive layer 54 coated on the sintered ceramic substrate 53 is burnt-off during the sintering. After 5 being sintered, the GS 55 shrinks and becomes a sintered ceramic substrate 57, and the conductive paste 52 becomes a sintered conductive pattern 56.

As described above, the sintered ceramic substrates 53 and unsintered ceramic green sheet 57 can be sintered together with the adhesive layer. That is, the sintered ceramic substrate 53 and GS 57 can be 10 bonded together with the adhesive, and thus, a multi-layer ceramic substrate having the sintered ceramic circuit board 53 and GS 57 combined with each other can be manufactured.

(Embodiment 6)

15 Fig.6 shows a method of manufacturing a ceramic circuit substrate in accordance with exemplary embodiment 6 of the present invention. An unsintered ceramic green sheet (hereafter referred to GS) having a via conductor is stacked on a sintered ceramic substrate having a conductive patterns formed by a film-intaglio-transfer-printing method.

20 At step 6a, a through-hole 62 is punched in a GS 61 with a puncher or the like. At step 6b, a conductive pattern 65 formed on an intaglio 64 is transfer-printed onto a sintered ceramic substrate 66 coated with an adhesive layer 67. Additionally, at step 6b, a via conductor 63 is formed in the through-hole 62 of the GS 61. At step 6c, the GS 61 having the via conductor 63 is heat-pressed onto the sintered ceramic substrate 66 having the conductive pattern 65 formed by the film-intaglio-transfer-printing method. The conductive pattern 65 formed by the film-intaglio-transfer-

printing method is adhered on the GS 61 with being embedded. At step 6d, the GS has a binder removed and is sintered. The adhesive layer 67 coated on a sintered ceramic substrate 66 is burnt-off during the sintering. After being sintered, the un-sintered GS 61 shrinks and becomes a sintered 5 ceramic substrate 69, and similarly, the conductive circuit 65 becomes a sintered conductive pattern 68.

As described above, a ceramic circuit substrate formed with stacked un-sintered ceramic green sheets having a via conductor placed on a sintered ceramic substrate is easily manufactured

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(Embodiment 7)

Fig.7 shows a method of manufacturing a ceramic circuit substrate in accordance with exemplary embodiment 7 of the present invention. A conductive pattern is formed on a sintered ceramic circuit substrate having a 15 via conductor provided in a through-hole by the a film-intaglio-transfer-printing method. An un-sintered ceramic green sheet (hereafter referred to as GS) is stacked on the substrate to be integrated.

At step 7a, a polyimide intaglio 71 is filled with conductive paste 72 into a groove formed by excimer laser with a ceramic blade or the like. As a 20 volume of the conductive paste shrinks when being dried, the intaglio is repeatedly filled with conductive paste and dried until the depth at the paste becomes 5 μ m pr less. At step 7a, the via conductor 75 are provided in the through-hole formed in a sintered ceramic substrate 73. Subsequently, the substrate 73 is coated with an adhesive layer 74 composed mainly of 25 thermoplastic resin at a thickness ranging approximately 1-10 μ m. At step 7b, the intaglio 71 filled with the conductive paste 72 is heat-pressed onto the sintered ceramic substrate 73. At step 7c, the intaglio 71 is separated

from the sintered ceramic substrate 73 after the conductive paste 72 is transfer-printed. At step 7d, an un-sintered ceramic GS 76 is heat-pressed onto the sintered ceramic substrate 73 where the conductive paste 72 is transfer-printed. In this heat-pressing process, the un-sintered ceramic GS 76 and sintered ceramic substrate 73, since both being bonded securely with the adhesive layer 74, do not remove from each other. At step 7e, the GS has a binder removed and is sintered. The Adhesive layer 74 coated on the sintered ceramic substrate 73 is burnt-off during the sintering. The GS 76 shrinks and becomes a sintered ceramic substrate 78, and similarly, the conductive paste 72 becomes a sintered conductive pattern 77. The ceramic substrate manufactured by this method exhibits little shrinkage in a direction in a plane. Thus, the GS shrinks in a thickness direction larger than in a stacking direction.

As described above, according to embodiment 7, a ceramic circuit substrate is easily manufactured from stacked un-sintered ceramic green sheets having a via conductor placed on a sintered ceramic substrate having a via conductor.

(Embodiment 8)

Fig.8 shows a method of manufacturing a ceramic circuit substrate in accordance with exemplary embodiment 7 of the present invention. A sintered ceramic substrate having a conductive pattern formed by a film-intaglio-transfer-printing method and an un-sintered ceramic green sheet (GS) are stacked for being integrated.

At step 8a, three ceramic substrates used in the manufacturing method of this embodiment, that is, the first ceramic substrate manufactured through step 7d in embodiment 7, the second ceramic

substrate manufactured through step 2b in embodiment 2, and the third ceramic substrate manufactured through step 6c in embodiment 6. In the first ceramic substrate, a GS 83 is stacked on a sintered ceramic substrate 81 having a via conductor 85 and conductive paste 82 formed by the film-intaglio-transfer-printing method. In the second ceramic substrate, a conductive pattern 82 is formed on the GS 83 by the film-intaglio-transfer-printing method. In the third ceramic substrate, the GS 83 having a via conductor 86 is stacked on a sintered ceramic substrate 81 having a conductive paste 82 formed by the film-intaglio-transfer-printing method.

At step 8b, the three substrates are stacked, heat-pressed for being integrated. The sintered ceramic substrate 81 and GS 83 are especially bonded easily with an adhesion layer 84. At step 8C, the GS has a binder removed and is sintered. The adhesive layer 84 coated on the sintered ceramic substrate 81 is burnt-off during the sintering. The GS 83 shrinks, and then, becomes a sintered ceramic substrate 88, and the conductive paste 82 becomes a sintered conductive pattern 87. The via conductor 86 formed on the GS 83 shrinks in the sintering together with the GS 83, then becomes a via conductor 89.

In embodiment 8, as shown at step 8a, the sintered ceramic substrate 81 and GS 83, both which are previously integrated, are stacked to another substrate for being integrated. Instead of being previously integrated, the sintered ceramic substrate 81 and GS 83, for example, may be integrated in a single operation at step 8b. Additionally, various kinds of multi-layer ceramic substrates can be manufactured by combined methods among those according to embodiments 1 to 7.

Examples according to the present invention will be explained below.

(Example 1)

Example 1 will be explained according to Fig.1. In the film-intaglio-transfer-printing method, a heat-resistant film such as polyimide is used for the intaglio. A groove for the conductive pattern is formed on the film with an excimer laser or the like. A parting treatment is provided with the film to be easily peeled off after the transfer-printing. With a rigid squeegee, the groove is filled with conductive paste to form the conductive pattern. A paste containing silver, since being capable of being sintered at a temperature of 850-900°C, is used as the conductive paste. The intaglio filled with the paste is dried in a drier for 5-10 minutes at a temperature of 100-150°C. The conductive paste filled in the groove shrinks in volume due to volatile evaporation of solvent in the paste. The intaglio has the groove filled with the conductive paste and is dried repeatedly until the depth at the paste from the no-groove formed surface of the intaglio becomes 5µm or less. Then, an adhesive layer composed mainly of thermoplastic resin is applied by, for example, a dipping method, screen printing method or spinner method, on the heat-resistant substrate made of, for example, a bake plate, metal plate or ceramic board or the like which does not deform under a drying temperature of approximately 150°C.. A softening point of the adhesive layer is lower than that of resin used as an organic binder for the un-sintered ceramic green sheet. From the intaglio filled with the conductive paste on the heat-resistant substrate coated with the adhesive layer, the conductive pattern is transfer-printed temporarily onto the heat-resistant substrate by the film-intaglio-transfer-printing method. In this process, a heat-pressing temperature is higher than the softening point of the adhesive layer but lower than 150°C. At this moment, a pressure of 30 to 80kg/cm² and its applying time of 3 to 10 minutes, are adjusted according to a status of the

conductive pattern.

Then, the conductive pattern is transfer-printed on the GS when the unfired ceramic GS is heat-pressed onto the heat-resistant substrate having the conductive patterns. Consequently, a surface of GS is provided with a fine conductive pattern formed by the film-intaglio-transfer-printing method. In this process, a heat-pressing temperature is necessarily higher than a softening point of the adhesive layer but lower than a critical point of heat deformation of the GS (approximately 150°C).

At this moment, a pressure of 50 to 150kg/cm² and its applying time of
10 3 to 10 minutes are adjusted according to a status of the conductive pattern.
The GS has the binder removed and is sintered, thus proving a ceramic
circuit substrate having a high-density conductive pattern. The GS, upon
being made of, for example, TMKLS-1000 (Nihon Denki Glass Co., Ltd.), can
be processed at a binder-removing temperature of 400 to 500°C and a
15 sintering temperature of 900°C, so that little residual carbon may not
remain in the GS

(Example 2)

Example 2 will be explained according to Fig. 2. Two or more ceramic green sheets (hereafter referred to as GS) both subjected to the film-intaglio-transfer-printing method in example 1 are heat-pressed. In this process, the heat pressing temperature is necessarily higher than a softening point of the adhesive layer but lower than a non-deformation temperature of the GS (approximately 100°C). A pressure of 50 to 100 kg/cm² and its applying time of 3 to 10 minutes are adjusted according to a status of the conductive pattern. A multi-layer ceramic circuit substrate having a high-density conductive pattern is manufactured after the GS has the binder removed and

is sintered.

(Example 3)

Example 3 will be explained according to Fig.3. A ceramic green sheet (hereafter referred to as GS) which is not subjected to the transfer-printing by the film-intaglio-transfer-printing method in example 1 has a through hole with a puncher. Having the through-hole filled with conductive paste by a screen printing method with a metal plate, the GS is dried under its heat deformation temperature of 60-100°C. Then, similarly to example 1, the GS having the via conductor and a heat-resistant substrate having a conductive paste temporarily transfer-printed are heat-pressed, thus transfer-printing the conductive paste again on the GS having the via conductor. Then, having the heat-resistant substrate removed, the GS having the conductive pattern and via conductor is manufactured. By this method, a ceramic circuit substrate having high-density conductive patterns connected between layers and via connection can be manufactured.

(Example 4)

Example 4 will be explained according to Fig.5. In the film-intaglio-transfer-printing method, a heat-resistant film such as a polyimide film is used for the intaglio. The film for the intaglio has a groove for a conductive pattern formed with excimer laser or the like. A parting treatment is applied to the film to be easily peeled off after a transfer-printing. The groove for the conductive patterns is filled with conductive paste with using a rigid squeegee. As the conductive paste, paste containing silver, which can be sintered at a temperature of 850-900°C, is used. An intaglio filled with the conductive paste is dried in a drier for 5-10 minutes at a

temperature of 100-150°C. The conductive paste filled into the groove shrinks in volume due to volatile evaporation of solvent in the paste. The groove is filled with the conductive paste and dried repeatedly until the depth at the paste from the upper surface not having the groove on the GS 5 becomes 5µm or less. Then, the sintered ceramic substrate is coated with an adhesive layer mainly composed of thermoplastic resin at a thickness ranging 1 to 10µm. by, for example, a dipping method, screen printing method, or spinner method. A softening point of the adhesive layer is lower than that of resin used as an organic binder for the un-sintered ceramic 10 green sheet. The intaglio film filled with the conductive paste on the sintered ceramic substrate coated with the adhesive layer, and then, a conductive pattern is transfer-printed on the sintered ceramic substrate by heat-pressing. Its heat-pressing temperature is necessarily higher than a softening point of the adhesive layer. A pressure of 30 to 80kg/cm² and its 15 applying time of 3 to 10 minutes are adjusted according to a status of the conductive pattern.

The sintered ceramic substrate, upon having the adhesive layer on sides thereof, provided on both sides with intaglio films which are filled with the conductive paste, so that the conductive patterns may be transferred 20 printed on both sides of the substrate. Then, an un-sintered ceramic green sheet (hereafter referred to as GS) is heat-pressed onto the sintered ceramic substrate having the conductive patterns to be integrated. In this process, a heat pressing temperature is required to be higher than a softening point of the adhesive layer but lower than a critical point for heat deformation of 25 the GS (approximately 100°C). In this heat-pressing process, a pressure of 50 to 150 kg/cm² and its applying time of 3 to 10 minutes are adjusted according to a status of the conductive patterns.

Then, the GS has the binder removed and is sintered, thus providing a ceramic circuit substrate having a high-density conductive pattern. The GS, upon being, for example, KLS-1000™ (Nihon Denki Glass Co., Ltd.), is processed at the binder removing temperature of 400-500°C and the sintering temperature of 900°C, so that little residual carbon can be remained in the GS

Thus, a un-sintered ceramic green sheet and sintered ceramic circuit substrate having a conductive pattern are stacked and heat-pressed to be integrated, and then, has a binder removed and is sintered, thus providing a ceramic circuit substrate having high-density conductive patterns easily manufactured

(Example 5)

Two sintered ceramic substrates having high-density conductive patterns in example 4 are stacked on both sides of a GS, and then, heat-pressed to be integrated, and has a binder removed and is sintered. This provides a ceramic circuit substrate having a high-density conductive pattern.

(Example 6)

A GS having a through-hole formed with a puncher or the like and provided with a via conductor formed by a screen printing method or the like is used as a GS in example 5. This provides a ceramic circuit substrate having a high-density conductive pattern electrically connected between layers therein

(Example 7)

A sintered ceramic substrate having a via conductor formed in a through-hole previously made is used as a sintered ceramic substrate in example 5. This provides a ceramic circuit substrate having high-density conductive patterns formed on both sides of the substrate and electrically connected to each other.

(Example 8)

A GS having a conductive pattern formed by a film-intaglio-transfer-printing method is used instead of the GS in examples 4 to 7 provides a 10 ceramic multi-layer circuit substrate having a high-density conductive pattern.

Industrial Applicability

As described above, according to the present invention, a ceramic circuit substrate having a multi-layer high-density conductive pattern can be easily manufactured by a film-intaglio-transfer-printing method.